241190US2 Docket No.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Takeshi YOSHIDA

SERIAL NO: 10/632,866

GAU:

2819

FILED:

August 4, 2003

EXAMINER: Daniel D. Chang

FOR:

SEMICONDUCTOR INTEGRATED CIRCUIT HAVING SYSTEM BUS DIVIDED IN STAGES

REQUEST FOR EXTENSION OF TIME UNDER 37 C.F.R. 1.136

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

It 1	s nereby requested that a one month extension of time be granted to April 10, 2003 for
	filing a response to the Official Action dated: December 10, 2004
	responding to the requirements in the Notice of Allowability dated:
	filing the Formal Drawings. The Issue Fee due has been timely filed.
	responding to the Notice to File Missing Parts of Application dated:
	filing a Notice of Appeal. A timely response to the final rejection, due has been filed.
	filing an Appeal Brief. A Notice of Appeal was filed on:
	Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee amount shown below is reduced by one-half.
The required fee of \$120.00 is enclosed herewith by credit card payment and any further charges may be made against the Attorney of Record's Deposit Account No. <u>15-0030</u> . A duplicate copy of this sheet is enclosed.	

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,

MAIER & NEWSTADT, P.C.

Eckhard H. Kuesters

Registration No. 28,870

Customer Number

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